

Curriculum Vitae

Personal Data

Title	Prof. Dr.-Ing.
First name	Milos
Name	Krstic
Current position	Professor "Design and test Methodology" (W2), at University of Potsdam Department Head "System Architectures" at IHP, Frankfurt (Oder)
Current institution(s)/site(s), country	University of Potsdam, IHP - Leibniz-Institut für innovative Mikroelektronik, Frankfurt (Oder)
Identifiers/ORCID	0000-0003-0267-0203

Qualifications and Career

Stages	Periods and Details
Dipl. Eng. Studies	Electronics & Telecommunications, 1992-1997, University of Nis, Serbia
M.Sc. Studies	Electronics, 1997-2001, University of Nis, Serbia
Doctorate	2006, supervisor: Prof. Dr. Rolf Kraemer, BTU Cottbus, Germany
Certified manager	Helmholtz-Akademie, 2010-2011, Malik Mng. Zentrum St. Gallen
Stages of academic/professional career	1997-2001, Teaching assistant, University of Nis, Serbia 2001-2006 PhD student, IHP, Frankfurt (Oder), Germany 2006-2007 Postdoc, IHP, Frankfurt (Oder), Germany 2007-2010 Project Leader, IHP, Frankfurt (Oder), Germany Since 2010 Group Leader, IHP, Frankfurt (Oder), Germany 2016 Call for professorship obtained from University of Potsdam (W2) and from Universität der Bundeswehr München (W3). The call from Munich was rejected and from Potsdam accepted. Since 2016 Professor, University of Potsdam, Germany 2020-2021 Acting Department Head "System Architectures", IHP, Frankfurt (Oder) Since 2021 Department Head "System Architectures", IHP, Frankfurt (Oder)

Selected Scientific Results

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Category A

- [1] L. Lu, J. Chen, A. Balakrishnan, M. Ulbricht, M. Krstic, Accelerate SEU Simulation-based Fault Injection with Spatio-temporal Graph Convolutional Networks, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), , doi: <https://doi.org/10.1109/TCAD.2025.3526748>, 2025
- [2] M. Fritscher, S. Singh, T. Rizzi, A. Baroni, D. Reiser, M. Mallah, D. Hartmann, A. Bende, T. Kempen, M. Uhlmann, G. Kahmen, D. Fey, V. Rana, S. Menzel, M. Reichenbach, M. Krstic, F. Merchant, and C. Wenger, A Flexible and Fast Digital Twin for RRAM Systems Applied for Training Resilient Neural Networks, Scientific Reports, <https://doi.org/10.1038/s41598-024-73439-z>, 2024
- [3] D. Zhao, O. Schrape, Z. Stamenkovic, M. Krstic, ImSTDP: Implicit Timing On-Chip STDP Learning, IEEE Transactions on Circuits and Systems I: Regular Papers, 10.1109/TCSI.2024.3450958, Vol. 72, Iss. 2, pp: 868-881, 2025.
- [4] Z. Su, S. Ramini, D. C. Marcolin, A. Veronesi, M. Krstic, G. Indiveri, D. Bertozzi, , S. Nowick, An Ultra-Low Cost and Multicast-Enabled Asynchronous NoC for Neuromorphic Edge Computing, IEEE Transactions on Emerging Topics in Computing, DOI 10.1109/JETCAS.2024.3433427 , Sep 2024, Vol. 14, Iss. 3, pp: 409-424.
- [5] L. Lu, J. Chen, M. Ulbricht, M. Krstic, Machine learning methodologies to predict the results of simulation-based fault injection, IEEE Transactions on Circuits and Systems I: Regular Papers, doi: 10.1109/TCSI.2024.3349928, Vol. 71, Iss. 5, pps: 1978-1991, 2024.
- [6] L. Lu, J. Chen, M. Ulbricht, M. Krstic, Towards Critical Flip-Flop Identification for Soft-Error Tolerance with Graph Neural Networks, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD) , Apr 2024, Vol. 43, Iss. 4, pp: 1135-1148, Digital Object Identifier: 10.1109/TCAD.2023.3331968. 2024
- [7] R.T. Syed, M. Andjelkovic, M. Ulbricht, **M. Krstic**, Towards Reconfigurable CNN Accelerator for FPGA Implementation, IEEE Transactions on Circuits and Systems II: Express Briefs, impact factor: 3.292, 2023, doi: 10.1109/TCSII.2023.3241154, Vol. 70, Iss. 3, pp: 1249-1253, 2023.
- [8] J. Chen, T. Lange, M. Andjelkovic, A. Simevski, L. Li and **M. Krstic**, Solar Particle Event and Single Event Upset Prediction from SRAM-based Monitor and Supervised Machine Learning, IEEE Transactions on Emerging Topics in Computing, impact factor: 7.7, doi: 10.1109/TETC.2022.3147376, Vol. 10, Iss. 2, pp. 564-580, 2022.
- [9] O. Schrape, M. Andjelkovic, A. Breitenreiter, S. Zeidler, A. Balashov, **M. Krstic**, Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops, IEEE Transactions on Circuits and Systems I: Regular Papers, impact factor: 3.318, doi: 10.1109/TCSI.2021.3109080, vol. 68, no. 11, pp. 4796-4809, Nov. 2021.
- [10] F. Kuentzer, **M. Krstic**, Soft Error Detection and Correction Architecture for Asynchronous Bundled Data Designs, IEEE Transactions on Circuits and Systems I: Regular Papers, doi: 10.1109/TCSI.2020.299891, 2020, impact factor: 3.318, Vol. 67, Iss. 12, pp. 4883-4894, 2020.

Academic Distinctions

Best paper award: 25th IEEE Latin-American Test Symposium (LATS2024),

Outstanding Student Presentation Award: 29th IEEE European Test Symposium 2024

Best Paper Award (Track): IEEE 33rd Annual International Symposium on Personal, Indoor and Mobile Radio Communications (PIMRC) 2022

Best Paper Award: Workshop on Microwave Theory and Techniques in Wireless Communications, MTTW'21

Best Paper Award (Special Session): 32nd International Conference on Microelectronics (MIEL), 2021

Best Paper Award: XXVII IEEE International Conference on Computer Design (ICCD), 2009

