

## Specification for the book of courses

<b>Study program</b>		Electrical Engineering and Computer Science		
<b>Module</b>		Common		
<b>Type and level of studies</b>		Doctoral studies		
<b>The name of the course</b>		Reliability Modeling of MOS Devices		
<b>Lecturer (for lectures)</b>		Danković M. Danijel		
<b>Lecturer/associate (for exercises)</b>				
<b>Lecturer/associate (for OFE)</b>				
<b>Number of ECTS</b>	10	<b>Course status (obligatory/elective)</b>	Elective	
<b>Prerequisites</b>				
<b>Course</b>		Introduction to basic accelerated testing for lifetime estimation in MOS transistors.		
<b>Course outcomes</b>		Theoretical knowledge. The ability to estimate the lifetime of MOS devices under normal operating conditions.		
<b>Course outline</b>				
<b>Theoretical teaching</b>	Accelerated tests for lifetime estimation in MOS transistor. Methods of designing experiments. Bias temperature stressing of MOS transistor. The influence of bias temperature stressing on the electrical characteristics of the transistor. Modeling of electrical parameters of MOS transistors. Voltage (classical model based on linear extrapolation, VG model, 1/VG model, power-law model E-n) and temperature models for lifetime estimation of MOS transistors under normal operating conditions. Determination of the experimental lifetime values. Lifetime estimation - extrapolation to normal operating conditions. Analysis of the influence of the selection of the electrical parameter, the failure criteria, the range of selected voltages and temperatures, and the chosen model for lifetime estimation.			
<b>Practical teaching (exercises, OFE, study and research)</b>				
<b>Textbooks/references</b>				
1	Danijel Danković, „Nestabilnosti p-kanalnog VDMOS tranzistora snage usled naponsko temperaturnih naprezanja sa negativnom polarizacijom gejta“, Elektronski fakultet u Nišu, magistarska teza, 2006.			
2	Danijel Danković, „Nestabilnosti komercijalnih VDMOS tranzistora snage usled naponsko temperaturnih naprezanja oksida gejta“, Elektronski fakultet u Nišu, doktorska disertacija, 2009.			
3	Tibor Grasser, Editor, „Bias Temperature Instability for Devices and Circuits“, Springer, 2014, ISBN 978-1-4614-7909-3.			
4	J.W. McPherson, Editor, „Reliability Physics and Engineering – Time-To-Failure Modeling“, Springer, 2014, ISBN 978-1-4419-6348-2.			
5	M. Tahori, Editor, „Microelectronics Reliability, Special Section: Modelling the Negative Bias Temperature Instability“, Elsevier, 2018, ISSN 0026-2714.			
<b>Number of classes of active education per week during semester/trimester/year</b>				
<b>Lectures</b>	<b>Exercises</b>	<b>OFE</b>	<b>Study and research work</b>	<b>Other classes</b>
3	0	0	0	0
<b>Teaching methods</b>	Theoretical teaching, active role in the scientific-research projects, seminars and projects.			
<b>Grade (maximum number of points 100)</b>				
<b>Pre-exam duties</b>	<b>Points</b>	<b>Final exam</b>	<b>Points</b>	
<b>Activity during lectures</b>		<b>Written exam</b>		
<b>Exercises</b>		<b>Oral exam</b>	50	
<b>Colloquia</b>				
<b>Projects</b>	50			